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23413 7590 03/23/2004

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Patricia DiGregorio

(Depositor's name)

Patricia DiGregorio

(Signature)

June 16, 2004

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/552,085	02/24/2000	Tsutomu Ishikawa		6265

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PADS WITH LESS INPUT SIGNAL ATTENUATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1330	\$0	\$1330	06/23/2004
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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NGUYEN, LONG T	2816	327-427000
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(A) NAME OF ASSIGNEE

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Sanyo Electric Co., Inc.

Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): individual corporation or other private group entity government

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06/22/2004 AWONDAF2 00000098 09552085

01 FC:1501
02 FC:8001

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